



Image  
B

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

NAKAGAWA, et al.

Serial No.: 09/810,499

Filed: March 19, 2001

Allowed: January 9, 2004

Group Art Unit: 9664

Examiner: Connie C. Yoha

**CONFIRMATION NO:** 9664

For: SEMICONDUCTOR MEMORY DEVICE WITH SHORTER SIGNAL LINES

**SUBMISSION OF FORMAL DRAWINGS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

*February 20 2004*

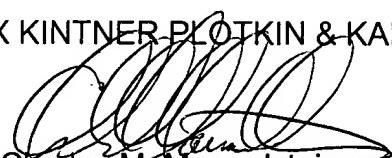
Sir:

In response to the Office Action dated November 20, 2002, and the requirement to file Corrected drawings in the Notice of Allowability dated January 9, 2004, enclosed are corrected formal drawings to be submitted in place of the informal drawings filed with the original application. It is respectfully submitted that these formal drawings overcome the objections to the originally filed drawings.

In the event that this paper is not timely filed, the applicants hereby petition for an appropriate extension of time. The fee for this extension may be charged to Deposit Account No. 01-2300, along with any other additional fees which may be required with respect to this paper.

Respectfully submitted,

ARENT FOX KINTNER PLOTKIN & KAHN, PLLC



Charles M. Marmelstein  
Reg. No. 25,895

Atty. Docket No.: 100353-00049

1050 Connecticut Avenue, N.W.  
Suite 400  
Washington, D. C. 20036-5339  
Tel (202) 857-6000  
Fax (202) 638-4810

CMM:mso

Enclosure: Corrected Drawings